

HC908JK3-Starter Kit

User's Manual

Brno
October 1 2001



Content

Introduction-----	5
Who is Beta Control-----	5
JK3EVB features -----	6
Software and Motorola CDMCU/D-----	7
Quick start instructions -----	8
MC68HC908 security feature-----	9
MON08 mode troubleshooting -----	10
JK3EVB hardware installation -----	12
Schematic description -----	13
JK3EVB schematic -----	14
JK3EVB part list-----	15
Parts location diagram and jumpers setting -----	16
Jumper setting for DBG08 kit-----	18

Introduction

The Motorola 68HC08 is a very strong and useful product family. The excellent FLASH memory, various peripheral subsystems, large variety of packages, programming and debugging on-chip support brings designer's dreams reality.

The Beta Control's Low Cost Starter Kit represents very good entry gate to design in. Various Target Boards could be used as a stand alone board or together with the universal debugger module. Some instant on board demo peripheral components and associated tutorial programs enable users to start immediately.

This is ideal tool for any distributors seminars, for the teachers at schools, users clubs and also for anybody who is interested in application with modern microcontrollers.

Jiri Gutman
Motorola consultant

Who is Beta Control

Beta Control Ltd. is an innovation based company. Its basic strategy consists in utilizing its own know how of modern technologies in the field of electronic control and information systems. More then sixty percent of employees are focused on design and development. As the most products of Beta Controls are based on Motorola technology and products, the Beta Control became a cooperation partner in promoting Motorola 68HC08 family.

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ISO 9001
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CZ-141/2000



JK3EVB features

- MCU 68HC908JK3CP
 - 4 Kbytes of on-chip FLASH memory with in-circuit programming capabilities
 - 128 bytes of on-chip RAM
 - 10-channel, 8-bit ADC
 - two 16-bit TIM channels
 - up to 15 general-purpose I/O pins
 - 20-pin plastic dual-in-line package (PDIP)
- in-circuit debugging
- low-cost development tools
- compatible with Motorola MON08 interface and ICS software tools
- power-on reset switch
- simple applications examples
 - 2 LED s
 - 2 push-buttons
 - temperature sensor LM35
- wire wrap area to easy build your application
- power supply range 8–12 V DC or direct 3 V or 5 V ($\pm 10\%$)
- dimensions 104x119 mm

Software and Motorola CDMCU/D

The HC908 Starter Kit is a hardware product only but you can't start your development work without software of course.

You can use various software products with HC908 Starter Kit.

There is a free Assembler in MCUez Package on the Motorola Microcontrollers double CD CDMCU/D enclosed in this package. You can also find some other software on the web but the best and highly recommended is a complex software package from P&E company. This Integrated Development Environment has been used for years for the Motorola 68HC05 Family and now it represents the best tool for 68HC08 Family also.

You can find this software at an internet address <http://www.pemicro.com> altogether with manuals and it is recommended to go there often to check the last versions of software and documentation. But you can find older versions also on the Motorola Microcontrollers double CD in the link `..\DEVTOOLS\0508sw.htm`

Motorola Microcontrollers double CD CDMCU/D is an excellent source of information. On this CD there is a lot of Data Books, Reference Manuals and Application Notes (including source code). The HC08 Family is fully upward object code compatible with the 68HC05 Family and you can use all application notes for HC05. You can use also the very good teaching book SMLMCUR2.PDF from the TUTORIAL folder.

If there will be some missing member of 68HC08 Family on CD in future, you can find information at the Motorola web site www.mot-sps.com under link Products.

Quick start instructions

For users experienced in installing Motorola or other development tools, the following steps provide a quick-start installation procedure for the JK3EVB hardware and software. If problems occur with the quick start procedures, refer to “Technical Reference and Troubleshooting” for troubleshooting instructions.

- Assemble and connect the hardware pod (see “Hardware Installation JK3EVB” and Electrostatic Discharge Caution). Make certain that power is off before performing this step.
- Install the ICS08JL software package. To start the software installation, run the program on CD ROM-Path: DEVTOOLS\SOFTWARE\DEVSW\ics08jzl_version_xxx.exe. During installation, follow the instructions in the installation wizard.
- Start the WinIDE editor and open a project file. Start the editor either from the Windows Start menu or by double clicking its icon. From the WinIDE Environment menu, choose the Open Project option, and choose a project file from the Specify project file to open dialog. Note that the environment is pre-configured. If no project file exists, choose the New option from the File menu to create a new project file. The desktop and environment settings made in the Environment Settings dialog are stored in the WIMDE.INI file and read each time the WinIDE editor is started. The project-specific desktop and environment settings can also be saved in a project file (*.PPF), which is read when the project is opened, allowing it to be saved and used as a general environment as well as custom environments for individual projects. To create the project file:
 - Specify the project-specific desktop and environment settings in the WinIDE editor.
 - Choose the Save Project As option from the WinIDE Environment menu to name and save the project to a directory folder.
- Assemble the code. Press the ASSEMBLE COMPILE FILE button (Hotkey F4) on the WinIDE toolbar to assemble the source code in the active WinIDE window.
- Run the ICS08 simulator. With a project or source file open in the WinIDE main window, click the In-Circuit Simulator button (Hotkey F6) on the WinIDE toolbar to start the ICS08 debugger and debug the contents of the active source window. If communications are not established with the pod, it may be necessary to select the proper port (COM1 or COM2) and baud rate 9600 Bd. Select Class III – Custom board with MON08 serial port circuitry built in. When communications are established, the DBG08 LED will not light. You can also use Simulator in stand-alone mode (Hotkey F9) without connected JK3EVB.
- Run the PROG08SW programmer. Press the PROGRAM button (Hotkey F7) on the WinIDE toolbar to start the programmer. Select Class III – Custom board with MON08 serial port circuitry built in and baud rate 9600 Bd.

- Run the ICD08SW real-time debugger. Press the REAL-TIME DEBUGGER button (Hotkey F8) on the WinIDE toolbar to start the ICD08SW in-circuit debugger and emulator. Select Class III – Custom board with MON08 serial port circuitry built in and baud rate 9600Bd.

ESD CAUTION: Ordinary amounts of static electricity from clothing or the work environment, can damage or degrade electronic devices and equipment. For example, the electronic components installed on printed circuit boards are extremely sensitive to electrostatic discharge (ESD). Wear a grounding wrist strap whenever handling any printed circuit board. This strap provides a conductive path for safely discharging static electricity to ground.

MC68HC908 security feature

The MC68HC908JK3 MCUs contain a security feature based on information programmed into the part. Security bytes are specified in addresses \$FFF6–\$FFFD. The PROG08SW software continually records any changes to these security bytes and stores them in the file SECURITY.INI.

The information in this file is also shared with the ICS08 in-circuit simulator and the ICD08SW in-circuit debugger software. This allows the user to reset the device and still have access to the monitor mode. The ICS08 software automatically attempts to access the part by trying the default (nothing written) and up to the last 10 sequences of bytes that have been written to the part.

If after trying each of the sequence of bytes stored in the SECURITY.INI file, the ICS08 software is unable to access the part, a security dialog box is displayed. This dialog allows the user to enter the security bytes manually or to read the information from the S19 file from which the MCU was last programmed.

MON08 mode troubleshooting

These instances include in-circuit simulation/emulation and FLASH memory programming. If difficulties are experienced when quick-start the kit using the procedure outlined in Quick Start Instructions, follow these steps.

- Disconnect any target cables from the board. These troubleshooting steps assume that no target system connections are present.
- Make sure that the MCU is installed correctly.
- Make sure that the DBG08 board is installed correctly.
- Make sure the board is getting power:
 - Check the power at the output of the adapter. First disconnect the pod from the power supply, then measure the power at the wall adapter's output connector to confirm that it produces appropriate dc voltages. If there is no power at the connector, verify that the adapter is getting power from the AC power outlet.
 - Check the power at the JK3EVB. First remove the DBG08 board from the JK3EVB, then plug the adapter's output connector into the JK3EVB. The system power LED VL1 should light. Check for 5V DC at the connector X8 on JK3EVB's. The Configuration header JP1 must be closed if power is from X1!
 - Check the pod system power with the DBG08 board attached. Disconnect the JK3EVB from the power supply. Configure the DBG08 board to the MON08 mode and attach the DBG08 board to the JK3EVB. Reconnect the power supply to the JK3EVB. The system power LED VL1 should light. If the LED does not light, may be there is a problem with the DBG08 causing too much of a drain on the 5V DC supply.
 - Check the DBG08 board's VTST output when the JK3EVB board is installed. It should be approximately 8.5V DC at JP2 pin 3. If the voltage is not present when the system power LED is lit, there may be a problem with the DBG08's internal step-up power supply.
- Make sure that the host PC can communicate with the MCU. If communication is correct, DBG08 VL1 LED will not light.
 - The MCU's PTB0 pin is used for host communications. DDRB bit 0 should never be set to 1 as this interrupts monitor-mode communications. The target connector PTB0 pins (X3 and X5 pin 12) are never connected to the MCU's PTB0 pin, when DBG08 is installed.
 - Make sure that the serial cable is correctly attached to the pod and to the correct serial port on the host computer.
 - Make sure that the cable is a straight-through cable supporting all nine pins of the serial port connection.
 - Make sure that no hardware security key or other devices are attached to the serial port or cable.

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- Make sure that the host PC supports the minimum speed requirements of the ICS08 software. Select Class III – Custom board with MON08 serial port circuitry built in and baud rate 9600 Bd.
 - Make sure to use the correct security code to access the MCU. If the security bytes have been programmed previously, the part will not unlock and enter monitor mode unless the correct security code is sent to the MCU.
 - Check for data at the pod end of the serial cable. TXD on pin 3 of this connector carries RS-232 data into the pod; RXD pin 2 carries RS-232 data out of the pod. DTR signal on pin 4 controls the MCU reset. Pin 5 is ground. While the ICS08 software is trying to establish communications, pins 3 and 4 should both toggle between +8V DC and –8V DC (or +12V DC and –12V DC). If these signals are not seen at the cable end, the problem is on the PC and cable side of the system. You can test it with ICS08 software because it detects hardware loop-back on startup PROG08SZ.
 - Make sure the serial data is getting to the MCU’s PTB0 pin.
- Make sure that the MCU has a good clock source. Use an oscilloscope to check the OSC input at the MCU. The clock frequency is a 9.83 MHz for a 9600-baud communications rate. If the clock signal is not present, check to see that the jumpers are installed (JP1 on DBG08 and JP3 on JK3EVB).
 - Make sure that the MCU can enter and remain in monitor mode. For this to happen, the following conditions must occur:
 - IRQ must be at VTST (from 7.5 to 9V DC) at the rising edge of RST. Using a dual-trace oscilloscope, trigger channel 1 on the rising edge of RST and read the IRQ pin with channel 2. Start the ICS08 software as described in Quick Start Instructions and verify that the signal is approximately 8.5V DC when RST rises. If IRQ is not at 8.5V DC, there may be a problem with the DBG08 board’s VTST circuit. Check JP2 position.
 - At the rising edge of RST, PTB0, PTB1, PTB2 and PTB3 must be held at definite logic values to enter into MON08 mode. With DBG08 installed, the MCU’s PTB0 pin is not connected to the target pins, as it is used for host communication.
 - Either RST or IRQ must remain at 8.5V DC to hold the MCU in monitor mode. The DBG08 board has an interrupt lockout feature to keep IRQ at 8.5V DC when the RST signal is asserted (low).
 - Make sure that external circuitry does not interfere with the monitor-mode communications. This ensures that the target system will not interfere with the communications and setup of the MCU’s monitor mode by allowing the pod to disconnect target system components during monitor mode entry.

JK3EVB hardware installation

This chapter explains how to:

- Configure the JK3EVB development board
- Assemble the JK3EVB board and the DBG08 serial interface to form the hardware pod and connect the pod to a host PC
- Connect the pod to a target system

In interactive mode, the JK3EVB is connected to the serial port of a host PC. The actual inputs and outputs of a target system can be used during simulation of source code. In stand-alone mode, the JK3EVB is not connected to the PC. The ICS08JL software can be used as a stand-alone simulator.

Configuring the JK3EVB

The configuration headers provide for jumper-selectable hardware options. Tables on page 16 describe these settings.

Assembling the target board with DBG08 for the MON08 mode

To assemble the target and prepare it for use with a host PC:

- Check the configuration headers on the target and DBG08 kits
- Install the DBG08 board onto the target MON08 connectors X2, X4. Make certain that power is off before performing this step.
- Connect the DBG08 to the host PC. Using the cable provided, connect it to a serial COM port on the host PC.
- Apply power to the target. Connect the correct power supply to the round connector on the JK3EVB. The green system power LED on the target should light.

NOTE: The JK3EVB's emulation of the on-board MCU is limited. PortB bit 0 (PTB0) is used for host-to-MCU communication. The port bit is not available for connection to a target system. Setting DDRB bit 0 to 1 will stop communications with the simulation or debugger software and will require a system reset to regain communication with the MCU. Port bits PTB1, PTB2 and PTB3 are temporarily disconnected from the target system during reset. Emulation of the MC68HC908JK's RST signal is also limited in that the signal is not an input signal. It is emulated as an output when using the target connectors.

Schematic description

The main component on the JK3EVB Kit board is a 68HC908JK3 microcontroller (DD1) from Motorola.

The main power supply is connected via the connector X1 and operating voltage range is from 8 V to 12 V DC. This input is protected against reversing of polarity by diode VD1. You can also connect 3 V or 5 V DC directly via connector X8. You can take off the JP1 to prevent LED VL1 and NR1 from effecting current consumption e.g. when you test special application supplied from 3 V DC. In this case the kit is protected by the NT1 but you have to use additional external fuse. It protects against the overvoltage above 6.8 V and reversing of polarity. Use the switch SA3 to generate POR if the kit is supplied via X1. VL1 signalizes the supply voltage for the kit.

To avoid problems whenever the voltage is below operating level the MCU contains low voltage inhibit module. This module monitors the voltage on the VDD pin and can force a reset when the voltage is below LVI trip falling voltage, VTRIP. The value of VTRIP is selected by the LVIT0, LVIT1 bits in the CONFIG1 register in the MCU.

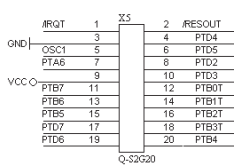
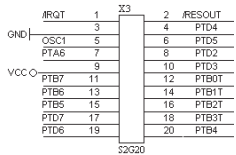
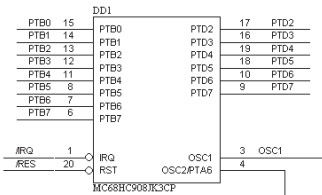
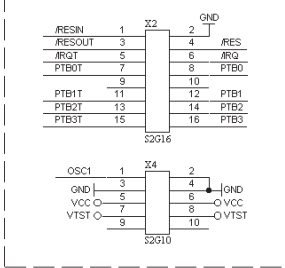
The MCU generates its clocks from a 16 MHz crystal connected to the OSC1 and OSC2 pins. The Kit has special jumper JP3 for clock selection. You must set the JP3 correctly for the normal operation (position labeled OSC) or special monitor mode (position labeled PTA6).

All controller signals are available via two double row header connectors X3 and X5. The connector X5 is not mounted as factory default to allow you to solder them up- or downward, depending on your application. Some signals are used by MCU to enter into the special monitor mode. You must short pins on connector X2 with jumper or install DBG08 development kit if your application needs them. The pins are: /RESOUT-/RES, /IRQT-/IRQ, PTB0T-PTB0, PTB1T-PTB1, PTB2T-PTB2, PTB3T-PTB3. The kit contains some simple applications and you can connect them with the MCU pins. You can connect analog input PTD3 via JP2 with the temperature sensor LM35. It converts temperature into voltage with constant step 10 mV/°C. JP3 and JP4 are for connection between the MCU pins and LEDs VL2, VL3. The kit contains two Leds and push buttons. The Leds light if MCU sets value 0 on pins PTD4 and PTD5, when the buttons are pushed down there are 0 on the MCU pins PTB5 and PTB6. The connector X8 is for the potentiometer that you can connect with analog input PTD2. The Port A pins of the MCU have internal pull-up resistors, so there shouldn't be problems leaving these pins unterminated.

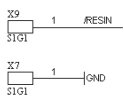
The connector X2 is a Motorola MON-08 compliant 16 pin header for the debug interface of the HC908. Some signals for special monitor mode (CLK, VTST...) are on connector X4. You may use them to connect a DBG08 kit pod while debugging a program or for programming the Flash. To enter into monitor mode you must take off JP3 from the position labeled OSC.

JK3EVB schematic diagram

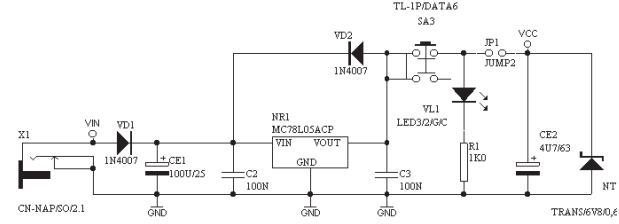
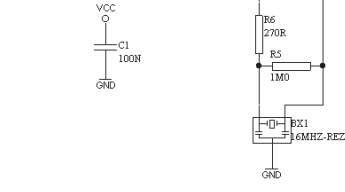
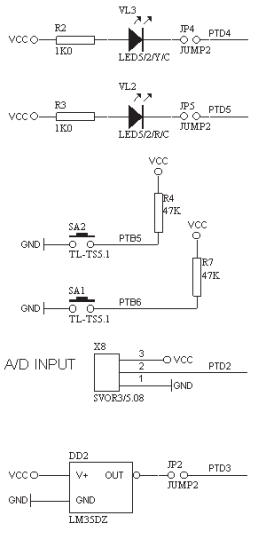
DEBUG CONNECTORS



NOT MOUNTED



APPLICATION EXAMPLES



JK3EVB part list

Part No.	Qty	Value	Typ
PCB			
1	PWB1	1	DPS DK2401 BOARD
IC			
2	DD1	1	MC68HC908KX8CP
3	DD2	1	LM35DZ
4	NR1	1	MC78L05ACP
Optical			
5	VL1	1	LED3/2/G/C L-934 LGD
6	VL2	1	LED5/2/R/C L-53 LID
7	VL3	1	LED5/2/Y/C L-53 LYD
Diode			
8	VD1, VD2	2	1N4007
9	NT1	1	TRANS/6V8/0,6 P6KE6.8ARL
Quartz			
10	BX1	1	16MHz KBR-16,0 MKD
Resistors			
11	R1	1	680R 491-0
12	R2, R3	2	330R 491-0
13	R5	1	1M0 491-0
14	R6	1	270R 491-0

Part No.	Qty	Value	Typ
Capacitors			
15	C1 - C3	3	100N C320C104K5R5CA7303
16	CE1	1	100U/25 THGS
17	CE2	1	4U7/63 THGS
Connectors			
18	X1	1	CN-NAP/SO/2.1 SCD-016
19	X2	0,2	S2G16 WWD 80 G
20	X3	0,25	S2G20 WWD 80 G
21	X4	0,125	S2G10 WWD 80 G
22	X5	0,25	Q-S2G20 WWD 80 G
23	X6	1	CN-ZAS/SO/2.1 SCP-2009B
24	X7, X9	0,05	S1G1 WWS 40 G
25	X8	1	SVOR3/5.08 MV 253/5,08
Others			
26	JP1, JP2, JP4, JP5	0,2	JUMP2 WWS 40 G
27	JP3	0,075	JUMP3 WWS 40 G
28	JS1 - JS10	10	JUMPER JUMPER SCHWARZ
29	PN1 - PN4	4	PNOZ-11/3,2 SJ-5201
30	SA1, SA2	2	TL-TS5.1 TS 062
31	SA3	1	TL-1P/DATA6 DATA MOM

Connectors X3 and X5

/RESOUT	PTD4	PTD5	PTD2	PTD3	PTB0T	PTB1T	PTB2T	PTB3T	PTB4
2	4	6	8	10	12	14	16	18	20
1	3	5	7	9	11	13	15	17	19
/IRQT	GND	OSC1	PTA6	VCC	PTB7	PTB6	PTB5	PTD7	PTD6

Connector X2

GND	/RES	/RQ	PTB0	NC	PTB1	PTB2	PTB3
2	4	6	8	10	12	14	16
1	3	5	7	9	11	13	15
/RESIN	/RESOUT	/IRQT	PTB0T	NC	PTB1T	PTB2T	PTB3T

Connector X4

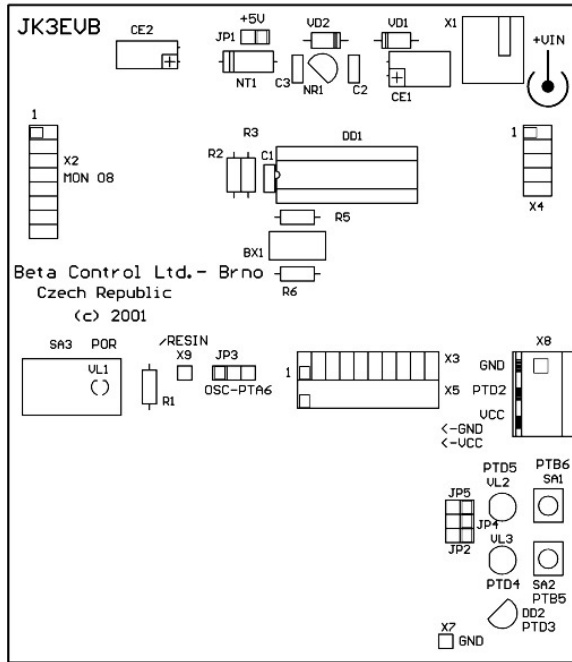
GND	GND	VCC	VTST	NC
2	4	6	8	10
1	3	5	7	9
OSC1	GND	VCC	VTST	NC

Connector X8

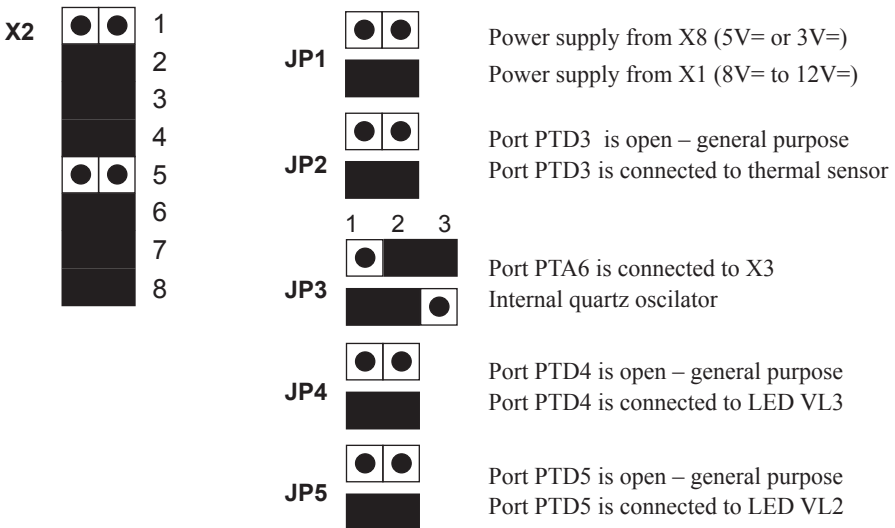
1	2	3
GND	PTD2	VCC

Parts location diagram and jumpers setting

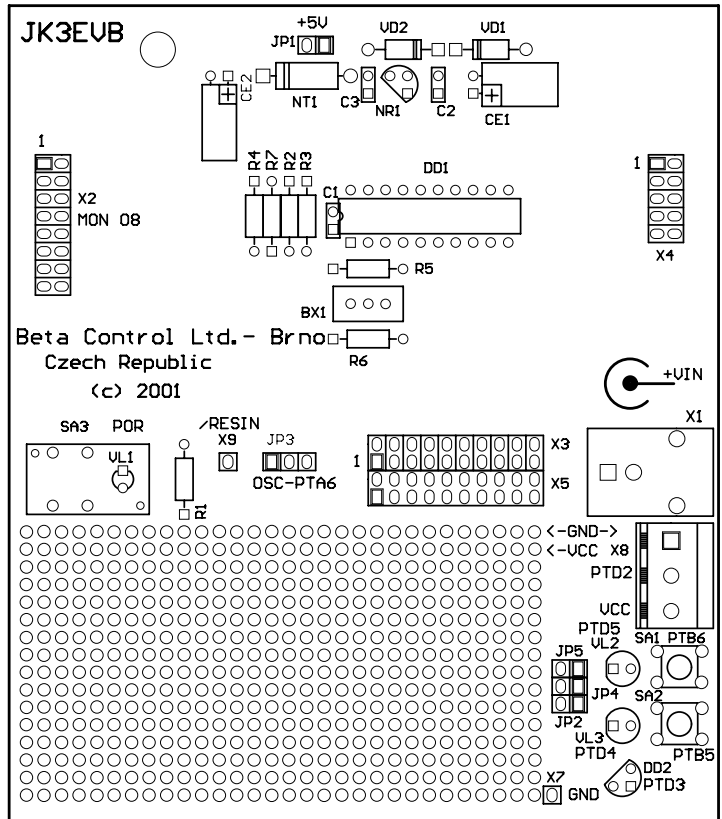
Version. 1.1



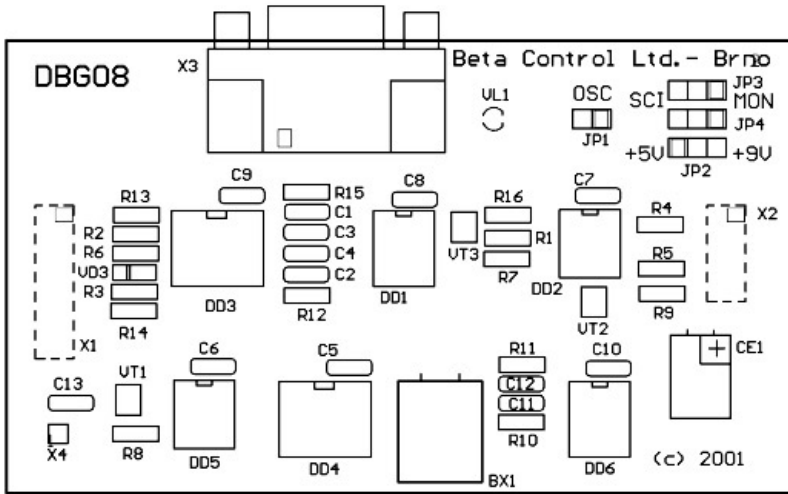
Present jumpers if use JK3EVB alone



Version. 1.2



Jumper setting for DBG08 kit



**JP1 - Configuration Header
– Oscillator Setting**

State	Signal Name	Description
ON	OSCOUT	Allows using 9,8304MHz DBG08 oscillator output
OFF	OSCOUT	Disconnect 9,8304MHz DBG08 oscillator output

**JP2 - Configuration Header
– VTST select**

Pins	Signal Name	Description
1 – 2	+5V	After reset the target MCU runs the user's application program
2 – 3	+9V	After reset the target MCU starts into MON08 mode

**JP3 - Configuration Header
– RX select (RS232)**

Pins	Signal Name	Description
1 – 2	COMIN	MON08 mode (position labeled MON)
2 – 3	RXD	User's application uses RS232 interface (position labeled SCI)

**JP4 - Configuration Header
– TX select (RS232)**

Pins	Signal Name	Description
1 – 2	COMM	MON08 mode (position labeled MON)
2 – 3	TXD	User's application uses RS232 interface (position labeled SCI)

